

Biosketch

Faculty Name: Pankaj Verma



Areas of Interest:

Optical Fiber Communication, Communication Engineering

Education

- M. Tech, KJ Somaiya, (Dr. APJ AKTU, Uttar Pradesh)
- B. Tech, IIT Lucknow (UPTU, Lucknow)

Academic/Industrial Experience

1. Associate Professor, Electronics & Communication Engineering, BHDTM, Lucknow, 2019-Present
2. Associate Professor, Electronics & Communication Engineering, BHDEC, Lucknow, 2015-2019
3. Lecturer / Assistant Professor, Electronics & Communication Engineering, BBDNBT, Lucknow, 2008-2015
4. Lecturer, Electronics & Communication Engineering, IIT, Bareilly, 2005-2008.

Selected Publications

1. Pankaj Verma, Ruchi Singh, and Prof. Y.K. Mishra on 'Modified GDS Technique - A Power Efficient Method For Digital Circuit Design' in International Journal of Electronics and Computer Science Engineering, Volume 2 No. 4, ISSN 2277-1956/ V2N4-1071-1080 published on 01/2013. (Citation-25, Global Impact factor-0.365)
2. Pankaj Verma, Ruchi Singh, and Dr. R.K.Singh on 'IMPROVED SOI DEVICE DESIGN USING SILVACO-TCAD SOFTWARE' in International Symposium on Recent Trends in Electronics & Communication (SRTEC 2012), at K.N.I.T., Sultanpur, India, on Nov-8-9 2012.
3. Ruchi Singh, Pankaj Verma and Dr. R.K.Singh on 'COMPARISON OF FD-SOI AND FD-SOI MOSFET' in International Symposium on Recent Trends in Electronics & Communication (SRTEC 2012), at K.N.I.T., Sultanpur, India, on Nov-8-9 2012.
4. Pankaj Verma, Ruchi Singh, and Dr. R.K.Singh on 'LOW TEMPERATURE SILICON WAFER- TO-WAFER FUSION BONDING' in National conference on ACCES-2012, at MMDC, Gorakhpur, India, ISBN NO. 978-93-82862-19-6, Page no. 272-276, on 12-13 April 2012.
5. Pankaj Verma, Ruchi Singh, and Prof. Y.K.Mishra on 'CHARACTERISTICS OF SOI MOSFETS AND ITS APPLICATIONS' in National conference on Challenges & Opportunities for Technological Innovation in India (COITI-2013), at Aarbhika Institute of Technology & Management, Lucknow, India, on Feb 16, 2013.